



Appl. No. 10/609,311

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/609,311
Filing Date June 26, 2003
Inventor Donald L. Yates
Assignee Micron Technology, Inc.
Group Art Unit 1756
Examiner Nicole M. Barreca
Attorney's Docket No. MI22-2236
Customer No. 021567
Title: Semiconductor Processing Patterning Methods

**RESPONSE TO JANUARY 23, 2006 FINAL OFFICE ACTION
TO ACCOMPANY REQUEST FOR CONTINUED EXAMINATION**

To: Mail Stop RCE
Commissioner for Patents
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AMENDMENTS